

Broadband SLD Light Source Module

Part Number: IPSDW0824-×××

1. Configuration

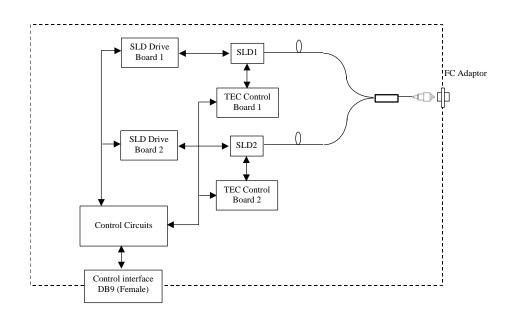


Figure 1 Configuration of IPSDW0824-xxxx SLD light source module

2. Absolute Maximum Ratings

Parameter	Min.	Max.	Unit
Power Supply Voltage	4.5	5.5	V
Storage Temperature	-40	+85	°C
Humidity	10	95	%

3. Recommended Operational Condition

Parameter	Min.	Typ.	Max.	Unit
Power Supply Voltage	4.75	5.00	5.25	V
Ripple/spike noise of Power Supply Voltage	-	50	120	mV_{p-p}
Operating Temperature (case)	15	25	50	°C
Operating Humidity (case)	30	60	90	%



4. Optical characteristics

Items	Specifications			Unit	Notes	
Items	Min.	Typ.	Max.	Unit	Notes	
Center Wavelength	830	850	870	nm	@25°C and CW.	
Total Optical Power	6	-	-	mW	Connectors are	
3dB Optical Bandwidth	95	-	-	nm	included.	
Wavelength Range @-6dB	790	-	920	nm		
ASE Ripple @ 0.1nm	ı	-	5	%		
Spectrum Flatness	ı	1.0	2.0	dB		
Optical Power Stability	-	-	±0.1	dB	Stability test of P _{max}	
(8hr)					after 0.5 hour warm up	
					at 25°C.	
Optical Output Type	Fiber pigtail or FC		-	As shown in Figure 2 of		
	Adaptor			Section 7 in detail		
Fiber Connector	FC/APC			-		
Fiber Type	Corning HI780 or			-		
	equivalent					
Fiber Jacket	900µm loose tube			-		
Fiber Length	50	-	-	cm	If pigtail fiber out is	
					selected.	

5. Electrical characteristics

Item	Specifications			Unit	Notes	
Item	Min.	Тур.	Max.	Omt	Notes	
Power supply current	-	1.2	2.5	A	P _{max} CW optical	
Power consumption	-	6.0	13.0	W	output	
Range of V _{set1} and V _{set2}	0.0	1	2.5	V		
Input impedance for V _{set1,2}	> 20k			ohm		
VH for TTL input/output	3.80	1	1	V	For SLD Enable and	
VL for TTL input/output	1	1	1.02	V	Alarm	
Optical Power Control	SLD Current		-			
	Adjustment via V _{set1}					
	and V _{set2} as shown in					
	Section 6					
Connector Type	DB9 Connector, Female			-	See section 6-Pin	
					Allocation in detail	



6. Pin Assignment Specifications

DB9 Connector (Female) Pin Allocation

Pin#	Function	In/Out	Type	Description
2	+5VDC	IN	Analog (5.0V)	Power Supply, ≤ 2.5 A.
3	SLD Enable	IN	TTL	SLD turn on control. TTL high turns on SLD and TTL low turns off the SLD. See Figure 3 in detail.
4	Alarm	OUT	TTL	TEC operation status. TTL high indicates TEC failure and TTL low indicates TEC operation is in normal. See Figure 3 in detail.
5	$ m V_{SET1}$	IN	Analog (0~2.5V)	Input voltage to set SLD1 current. The range of 0.0-2.5V for V_{SET1} corresponds to $0 \sim I_{1max}$ mA of SLD1 operation current.
6 7	GND	IN	GND	Power supply and signals GND.
8	$ m V_{SET2}$	IN	Analog (0~2.5V)	Input voltage to set SLD2 current. The range of 0.0-2.5V for V _{SET2} corresponds to 0 ~I _{2max} mA of SLD2 operation current.
9	NC	NA	NA	Reserved

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7. Mechanical Specifications

4. Drawing and dimensions (unit: mm)

Size: $120mm (L) \times 70mm (W) \times 36mm (H)$

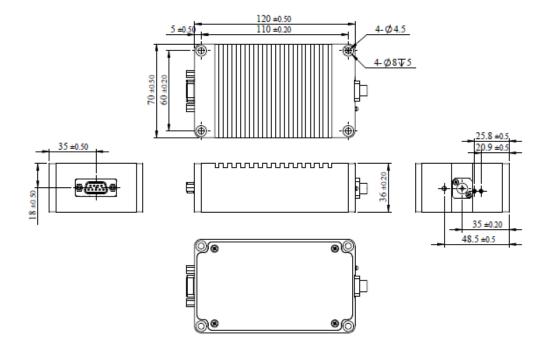


Figure 2 Mechanical drawing of IPSDW0824-0314 module

2. Module case is isolated from any electrical connection.

8. Signals Characteristics

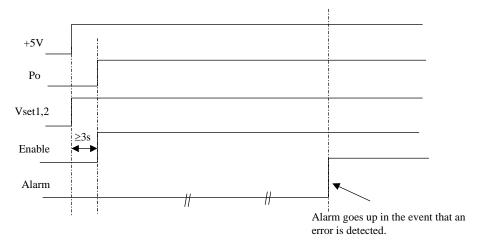
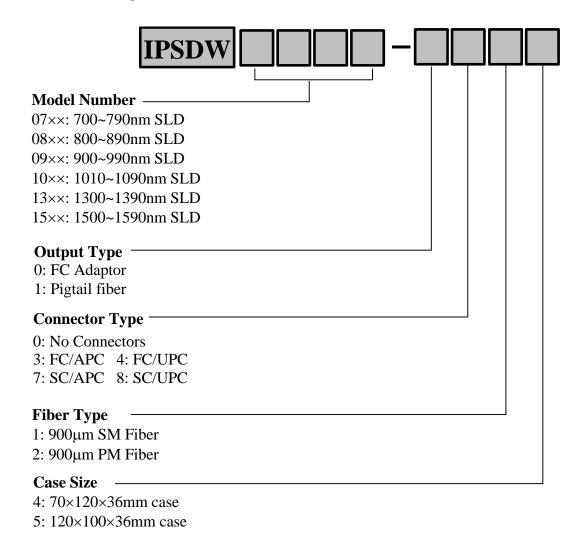


Figure 3 Startup and operational timing of IPSDW0824 module

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9. Part Numbering Structure



Example: IPSDW0821-0314: 800nm-type SLD module in 70×120×36mm case with FC adaptor output, FC/APC connector and 900μm SM Fiber.

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